PATENT

S/N Unknown



Applicant:

Dmitriy Rumynin et al.

Examiner: Unknown

Serial No.:

Unknown

Group Art Unit: Unknown

Filed:

Herewith

Docket: 1365.039US1

Title:

A PARALLEL COUNTER AND A MULTIPLICATION LOGIC CIRCUIT

PRELIMINARY AMENDMENT

BOX PATENT APPLICATION

Commissioner for Patents Washington, D.C. 20231

Dear Sir:

Prior to calculation of the filing fee and examination of the above-identified patent application, please amend as follows.

IN THE SPECIFICATION

On the first page, after the title, please insert:

Cross-Reference to related Applications

This application is a continuation-in-part of United States Application Serial Number 09/637,532, filed on August 11, 2000, which claims priority of Great Britain Application No. 0019287.2, filed on August 4, 2000, all of which are incorporated herein by reference.

IN THE CLAIMS

In claim 18, line 3, please delete "any one of claims 1 to 15" and insert --claim 1-therefor.

In claim 19, line 3, please delete "any one of claims 1 to 15" and insert --claim 1-therefor.

In claim 20, line 3, please delete "any one of claims 1 to 15" and insert --claim 1-therefor.

In claim 41, line 2, please delete "any one of claims 1 to 16 or 21 to 35" and insert --claim 1-- therefor.